

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently amended) A method for displaying and modifying timing data generated by an EDA tool used to simulate a circuit being designed, the method comprising:  
receiving the timing data from the EDA tool, the timing data including at least multiple periods of a plurality of clock signals;  
selecting first and second clock signals from the plurality of clock signals based on input received from a user;  
generating a first waveform for the first selected clock signal and a second waveform for the second selected clock signal using the timing data;  
generating a third waveform characterized as being a delayed replica of the first waveform and having transitions defining at least one launch edge;  
generating a fourth waveform characterized as being a delayed replica of the second waveform and having transitions defining at least one latch edge;  
selecting for display a portion of each of the first, second, third and fourth waveforms, the displayed portion of the third waveform comprising the at least one launch edge and the displayed portion of the fourth waveform comprising the at least one latch edge;  
displaying pointers to time points of interest on the displayed waveform portions;  
receiving edits to the time points of interest in response to a user moving the pointers on an interactive graphical user interface; and  
modifying the circuit so as to cause a change in relative timing of the at least one launch edge and the at least one latch edge based on the edits to the time points of interest.
2. (Canceled)
3. (Canceled)

4. (Canceled)

5. (Currently amended) The method of claim 1 wherein:

the time duration of the display of the first, second[[.]], third and fourth waveforms in a window of the interactive graphical user interface is a time period selected to be a multiple of the least common multiple of periods of the first clock signal and the second clock signal.

6. (Canceled)

7. (Canceled)

8. (Canceled)

9. (Currently amended) The method of claim 1 ~~wherein:~~ further comprising:

updating [[the]] timing parameters based on the edits to the time points of interest ~~further comprises by~~ inverting the launch edge of the first clock signal, either in a design file or as an input to a static timing verification tool.

10. (Currently amended) The method of claim 1 ~~wherein:~~ further comprising:

updating [[the]] timing parameters based on the edits to the time points of interest ~~further comprises by~~ inverting the latch edge of the second clock signal.

11. (Currently amended) A computer-readable medium encoded with a

computer program, the computer program comprising a set of instructions for displaying and modifying timing data generated by an EDA tool used to simulate a circuit being designed, wherein the set of instructions when executed by a computer causes the computer to:

receive the timing data from the EDA tool, the timing data including at least multiple periods of a plurality of clock signals,

enable selection of first and second clock signals from the plurality of signals based on an input received from a user;

generate a first waveform for the first selected clock signal and a second waveform for the second selected clock signal using the timing data;  
generate a third waveform characterized as being a delayed replica of the first waveform and having transitions defining at least one launch edge;  
generate a fourth waveform characterized as being a delayed replica of the second waveform and having transitions defining at least one latch edge;  
select for display a portion of each of the first, second, third, and fourth waveforms, the displayed portion of the third waveform comprising the at least one launch edge and the displayed portion of the fourth waveform comprising the at least one latch edge;  
display pointers to time points of interest on the displayed waveform portions;  
receive edits to the time points of interest in response to a user moving the pointers on [[the]] an interactive graphical user interface; and  
modify the circuit so as to cause a change in relative timing of the at least one launch edge and the at least one latch edge based on edits to the time points of interest.

12. (Previously presented) The computer-readable medium according to claim 11 wherein  
the display of the portion of each of the waveforms in the interactive graphical user interface further comprises display of each of the portions of the waveforms in synchronism.
13. (Canceled)
14. (Canceled)
15. (Canceled)
16. (Previously presented) The computer-readable medium according to claim 11 wherein the display of the pointers to the time points of interest on the waveforms further comprises:

display of a first pointer to a launch edge of the third waveform that triggers a first latch to capture a data signal; and

display of a second pointer to a latch edge of the fourth waveform that triggers a second latch to capture the data signal.

17. (Currently amended) The computer-readable medium according to claim 16 wherein the display of the pointers to the time points of interest on the waveforms further comprises:

display of a third pointer to an edge of the first waveform that corresponds to [[the]] a source multi-cycle edge of the first waveform; and

display of a fourth pointer to an edge of the second waveform that corresponds to [[the]] a destination multi-cycle edge of the second waveform.

18. (Currently amended) The computer-readable medium according to claim 16 wherein the set of instructions when executed by the computer further causes the computer to generate

[[the]] a new timing parameters based on the edits to the time points of interest by changing a multi-cycle value that represents a number of active edges in the first clock signal from the launch edge to the latch edge in response to the user moving the first or the third pointer on the interactive graphical user interface.

19. (Currently amended) The computer-readable medium according to claim 17 wherein the set of instructions when executed by the computer further causes the computer to generate

[[the]] a new timing parameters based on the edits to the time points of interest by changing a multi-cycle value that represents a number of active edges in the second clock signal from the launch edge to the latch edge in response to the user moving the second or the fourth pointer on the interactive graphical user interface.

20. (Previously presented) The computer-readable medium according to claim 11 wherein the circuit design is a design for a field programmable gate array.

21. (Previously presented) The computer readable medium according to claim 11 wherein the set of instructions when executed by the computer further causes the computer to generate updated waveforms for the signals using updated timing data, wherein the EDA tool generates the updated timing data by compiling, simulating and performing verification analysis on the circuit design using the new timing parameters and displays the updated waveforms in the interactive graphical user interface.